

09/601192

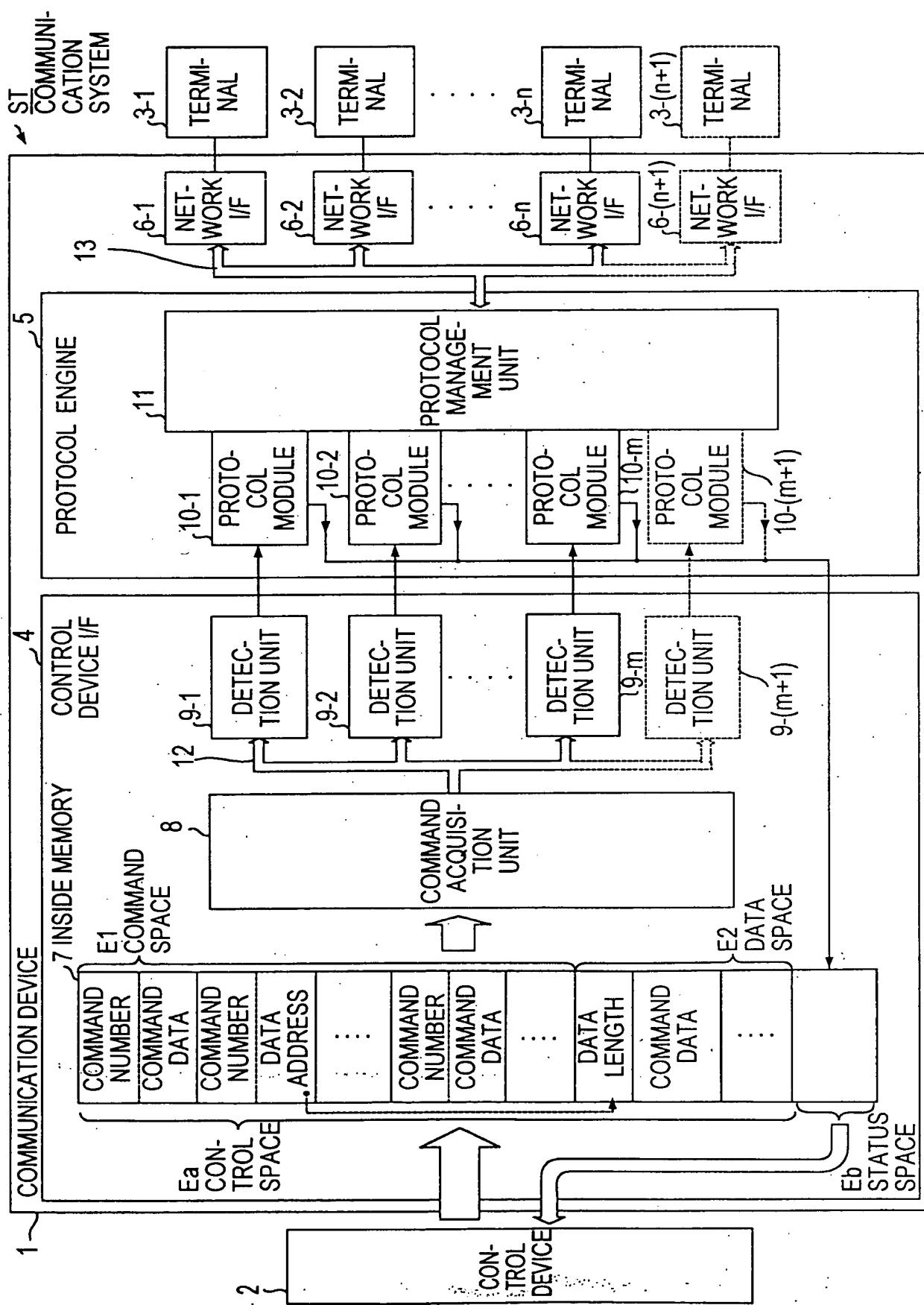


FIG. 1

ADDRESS SPACE  
OF CONTROL  
DEVICE 2

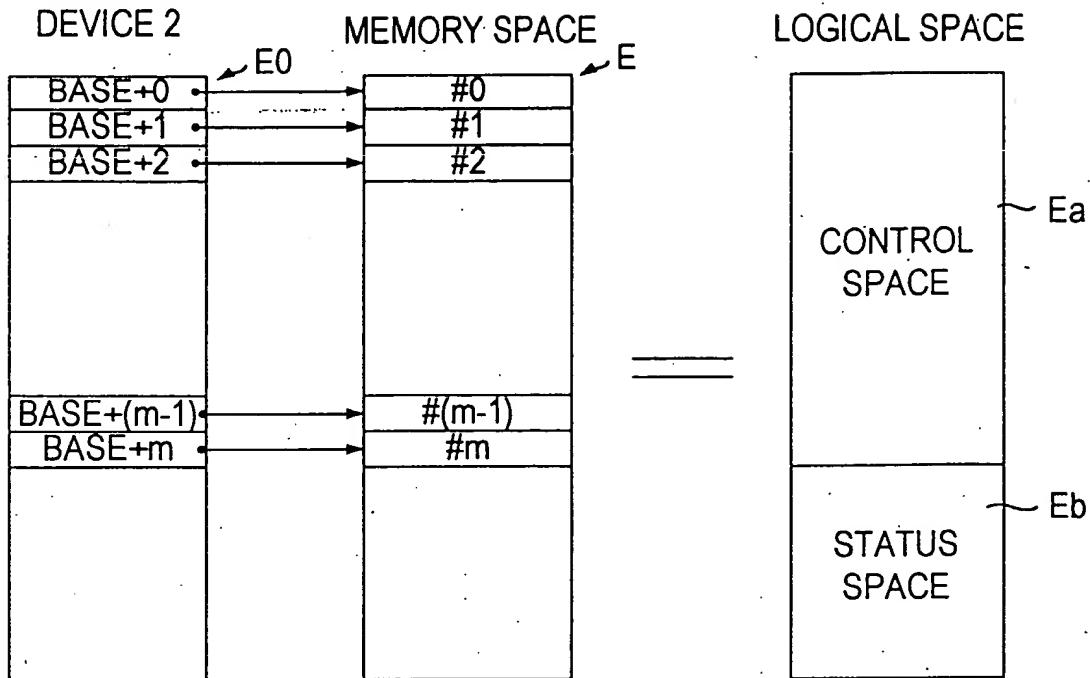


FIG. 2

LOGICAL SPACE

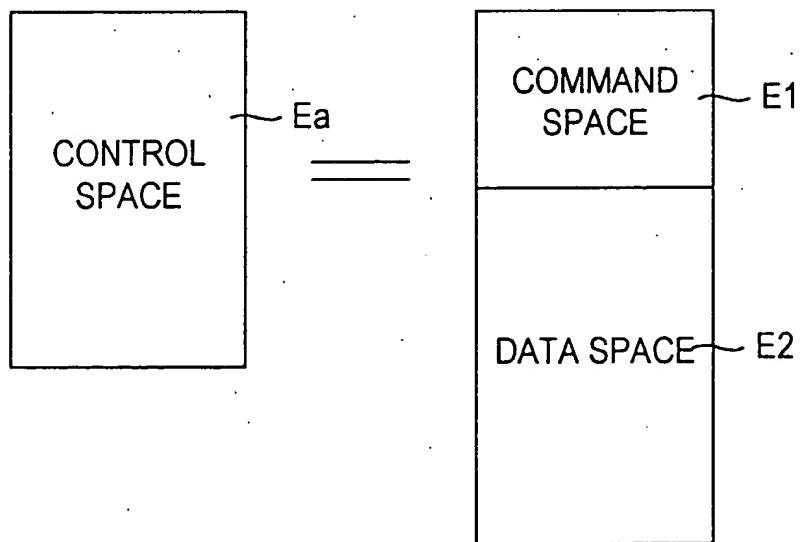


FIG. 3

### B : MEMORY (BYTE) WIDTH

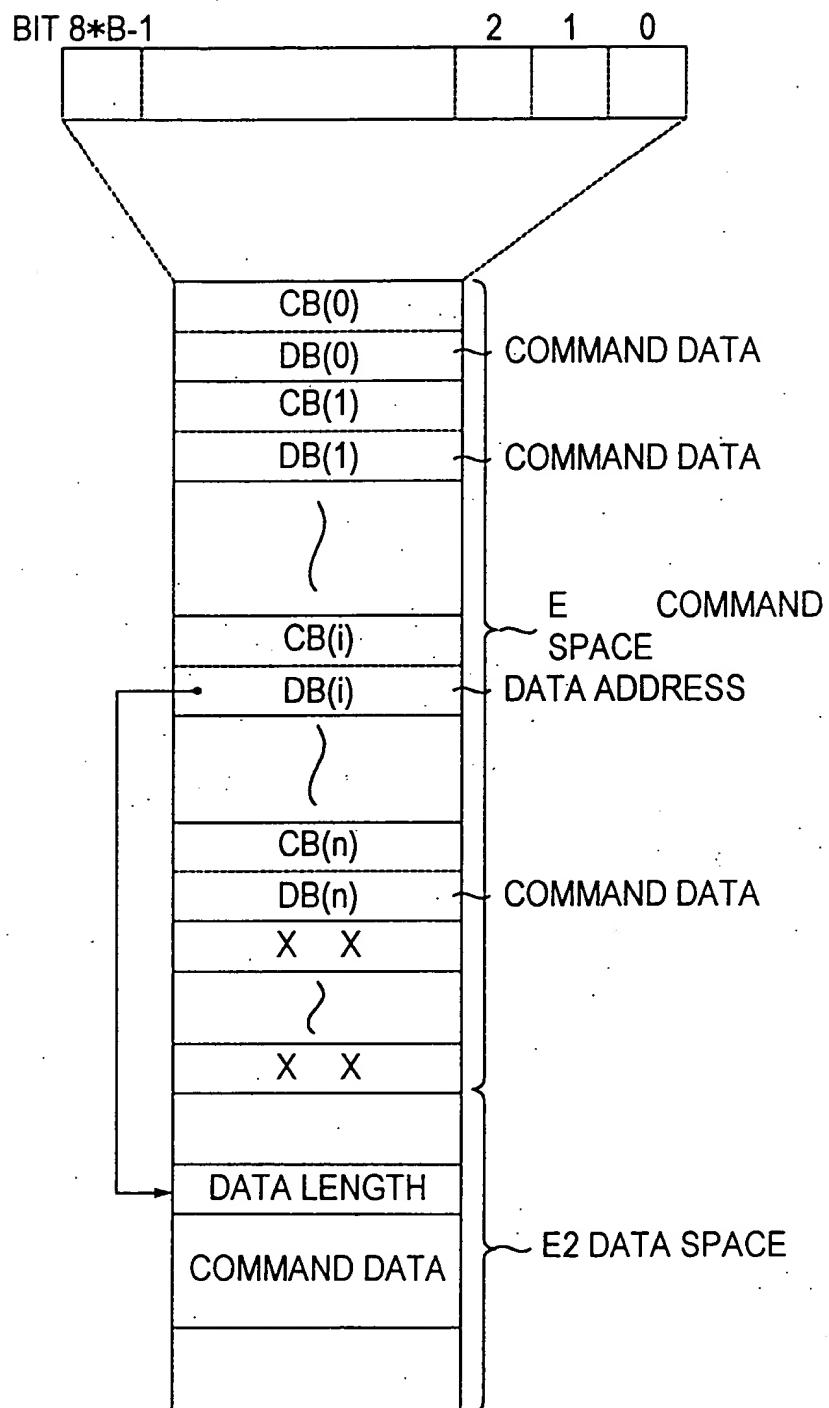


FIG. 4

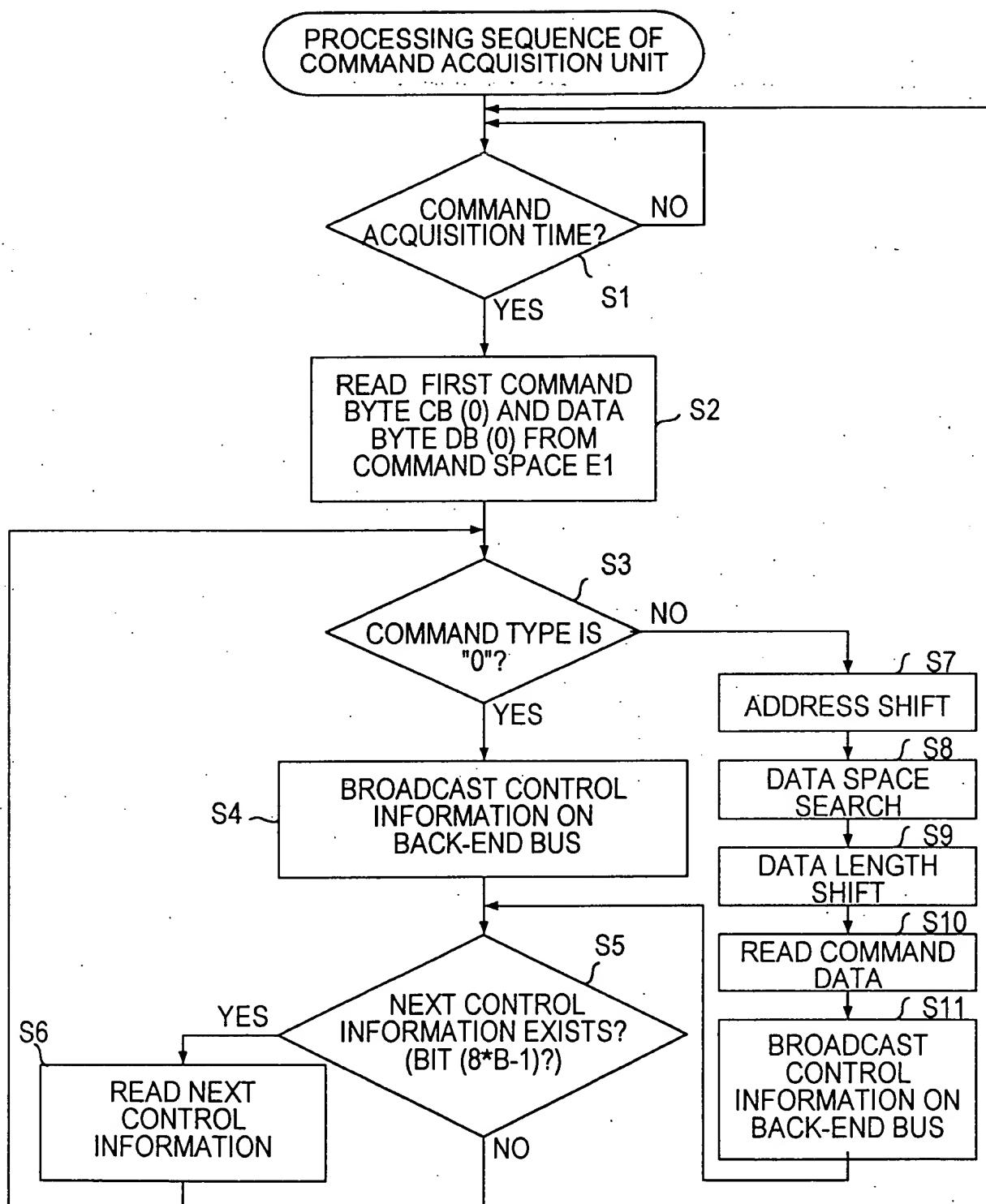


FIG. 5

00000000000000000000000000000000

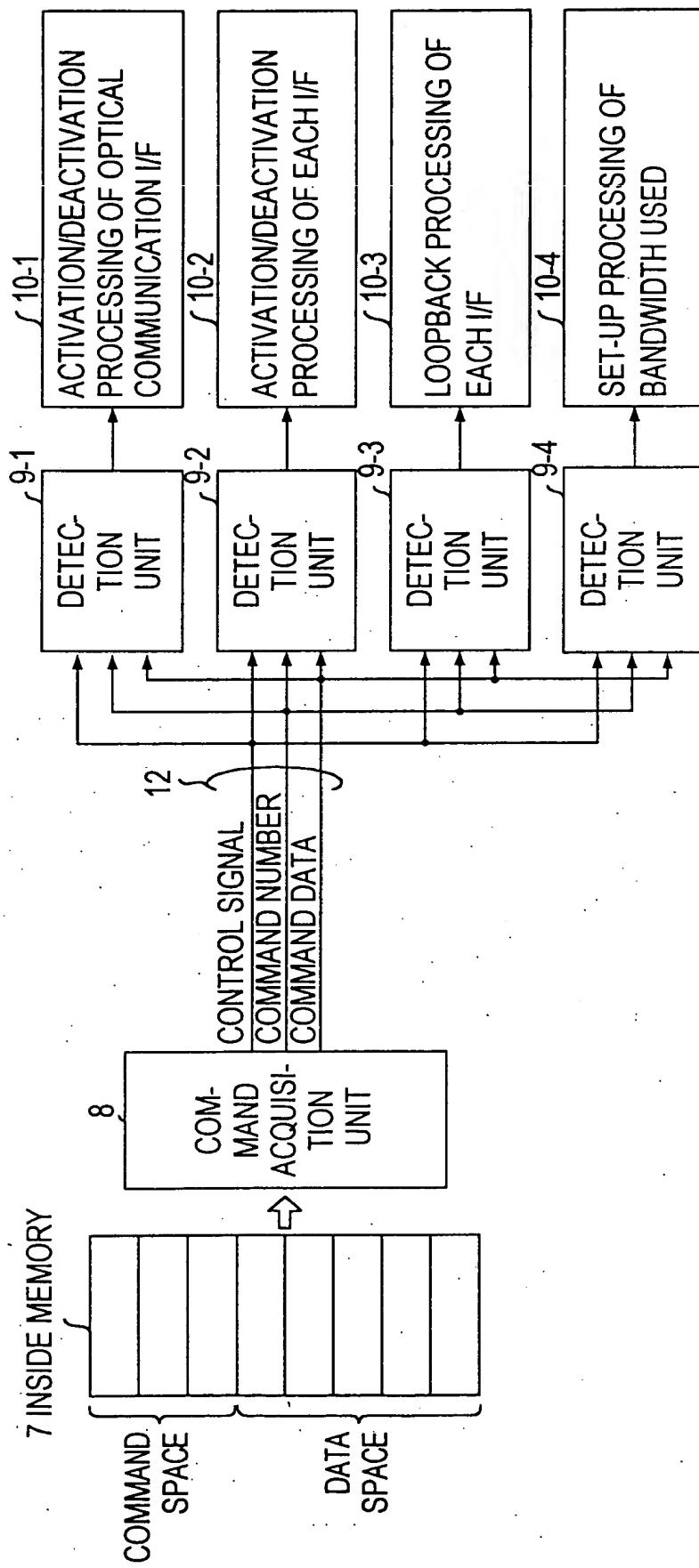


FIG. 6

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## COMMAND SPACE

BASE+0	1 00 00010	CB(0)	DEACTIVATION
BASE+1	100 0000 0	DB(0)	OF VIDEO I/F.
BASE+2	0.00 00011	CB(1)	LOOPBACK OF AUDIO
BASE+3	011 0000 1	DB(1)	I/F AND DATA I/F

FIG. 7(a)

## COMMAND SPACE

BASE+0	1 00 00001	CB(0)	DEACTIVATION OF OPTICAL
BASE+1	0000000 0	DB(0)	COMMUNICATION I/F
BASE+2	0 01 00100	CB(1)	SET-UP OF BANDWIDTH
BASE+3	VIRTUAL ADDRESS ( $\alpha$ )	DB(1)	TO BE USED

## DATA SPACE

BASE+	0000 0011	DATA LENGTH = 3 BYTES
BASE <sub>1</sub>	0001 0101	AUDIO BANDWIDTH = 15%
BASE <sub>1+1</sub>	0101 0000	DATA BANDWIDTH = 50%
BASE <sub>1+2</sub>	0011 0101	VIDEO BANDWIDTH = 35%
1+3		

FIG. 7( b)

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01022010 \* 26 T T 0960

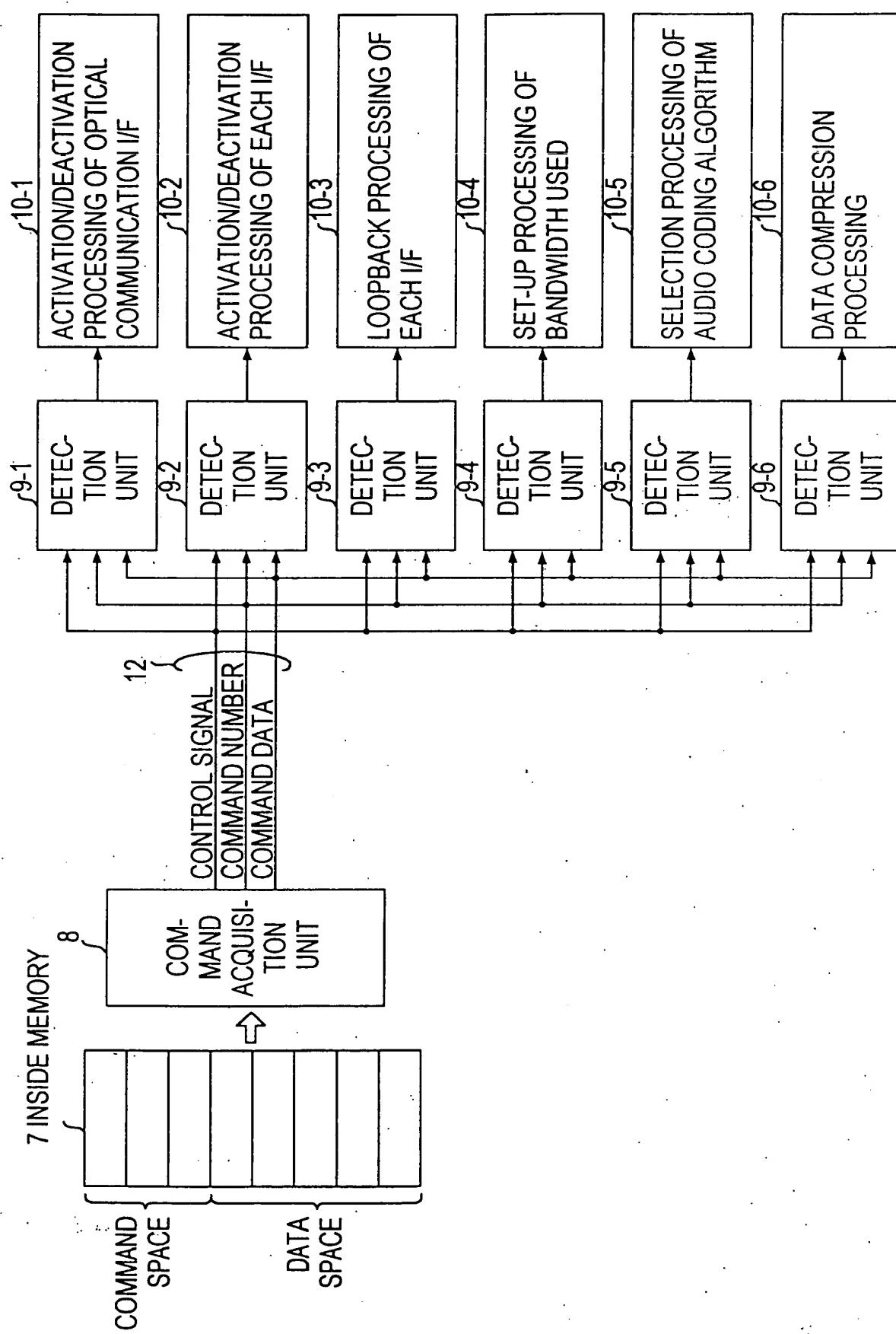


FIG. 8

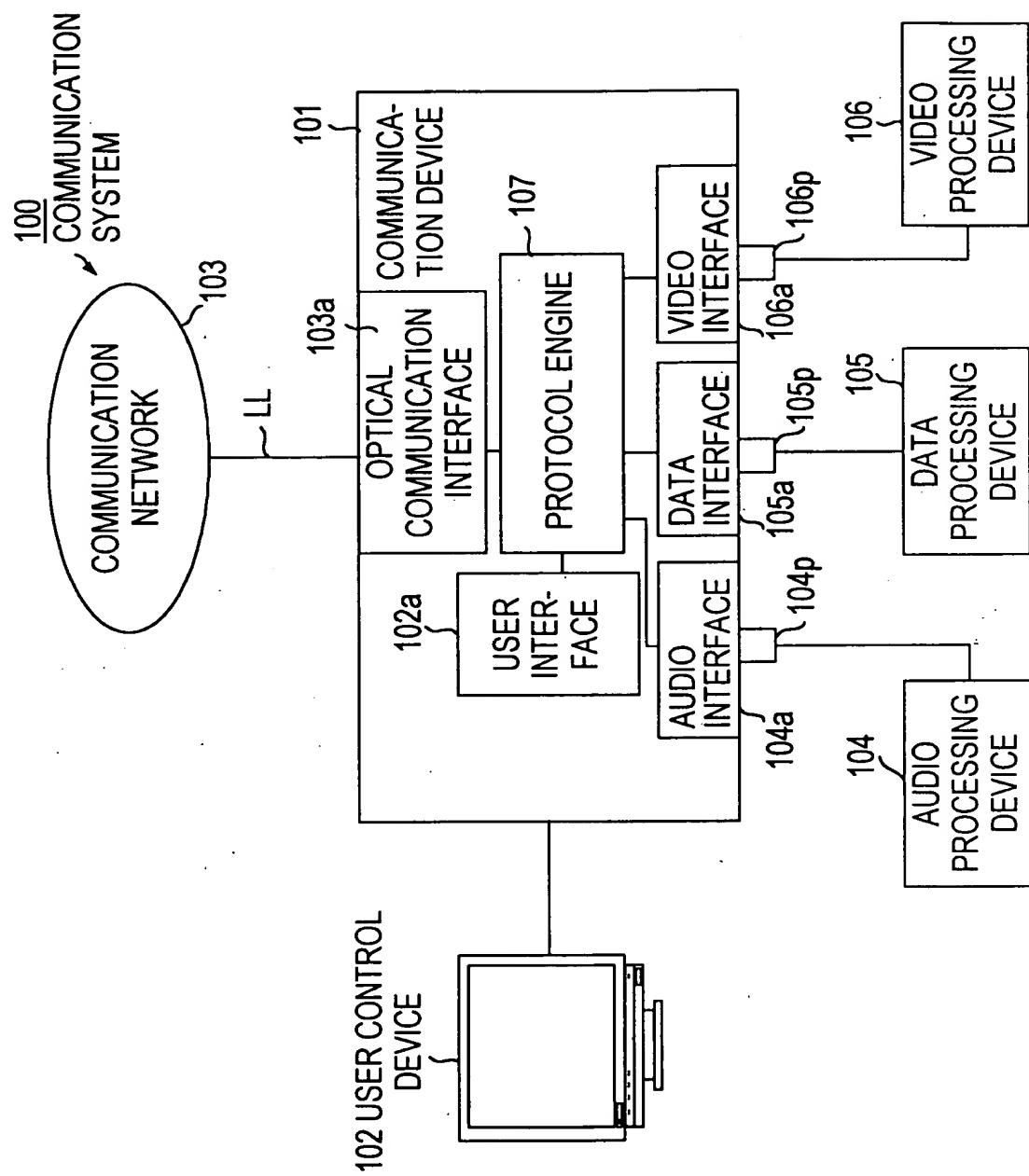


FIG. 9

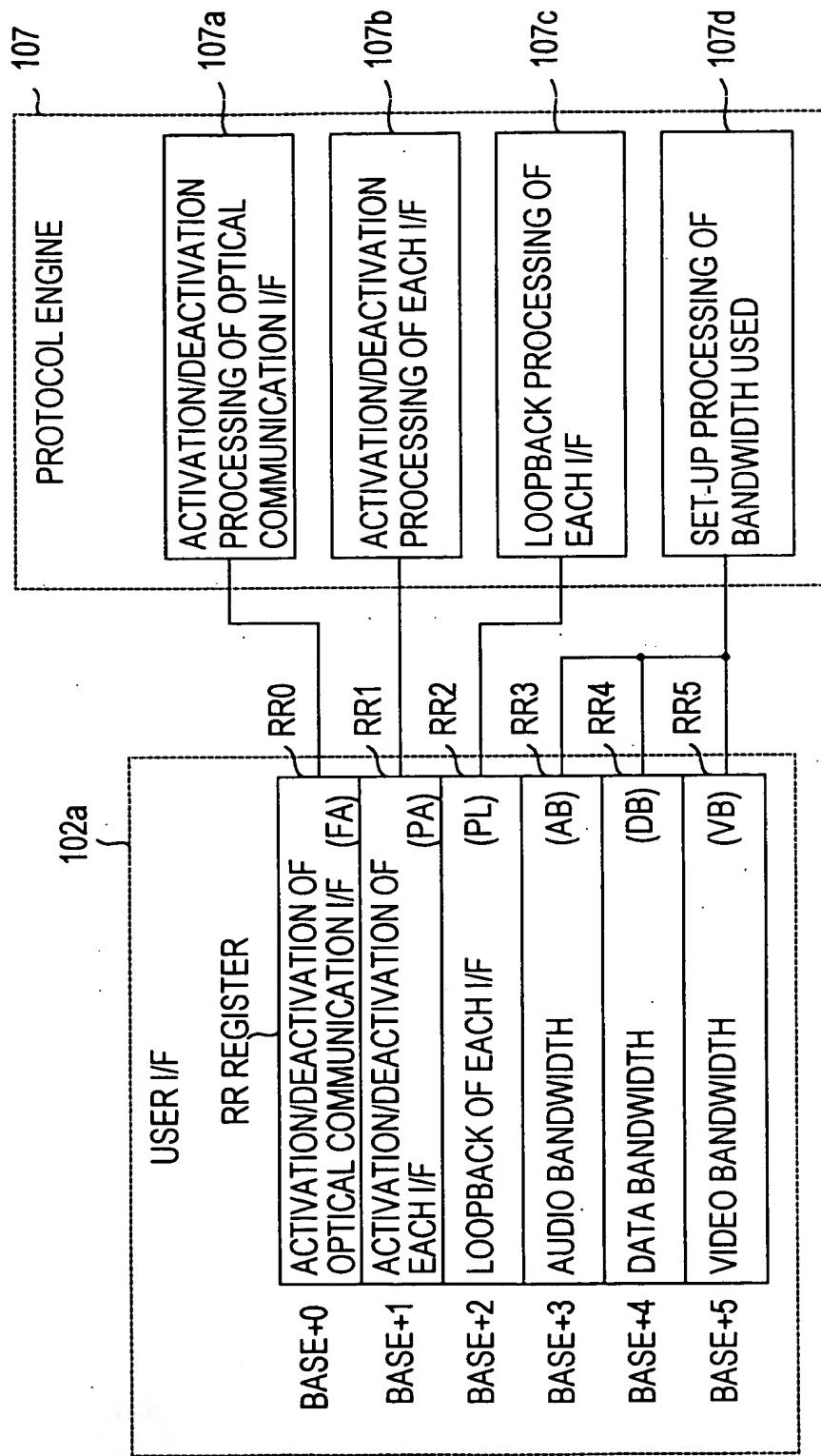


FIG. 10

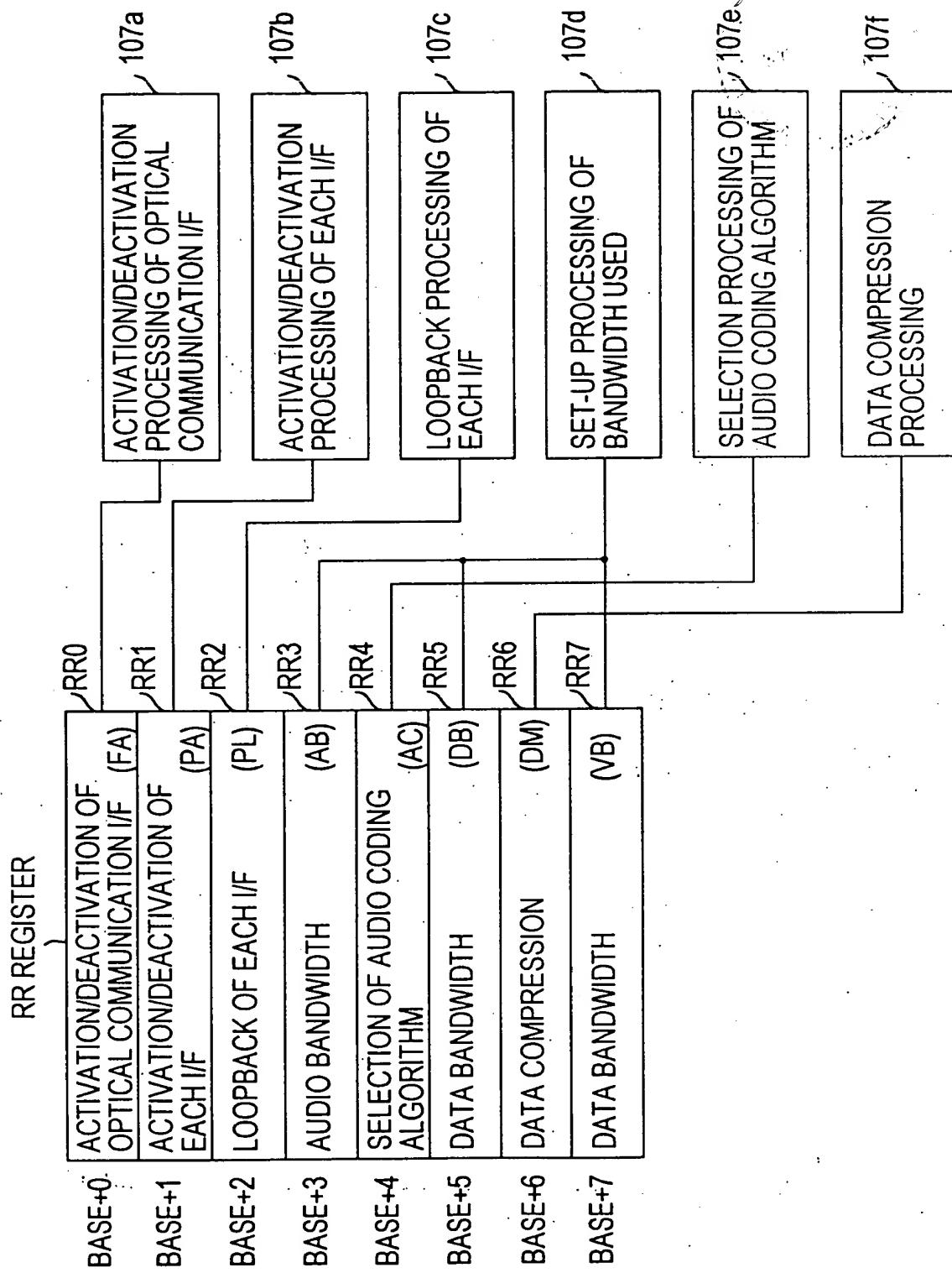


FIG. 11

008220 " 26 TT 0950

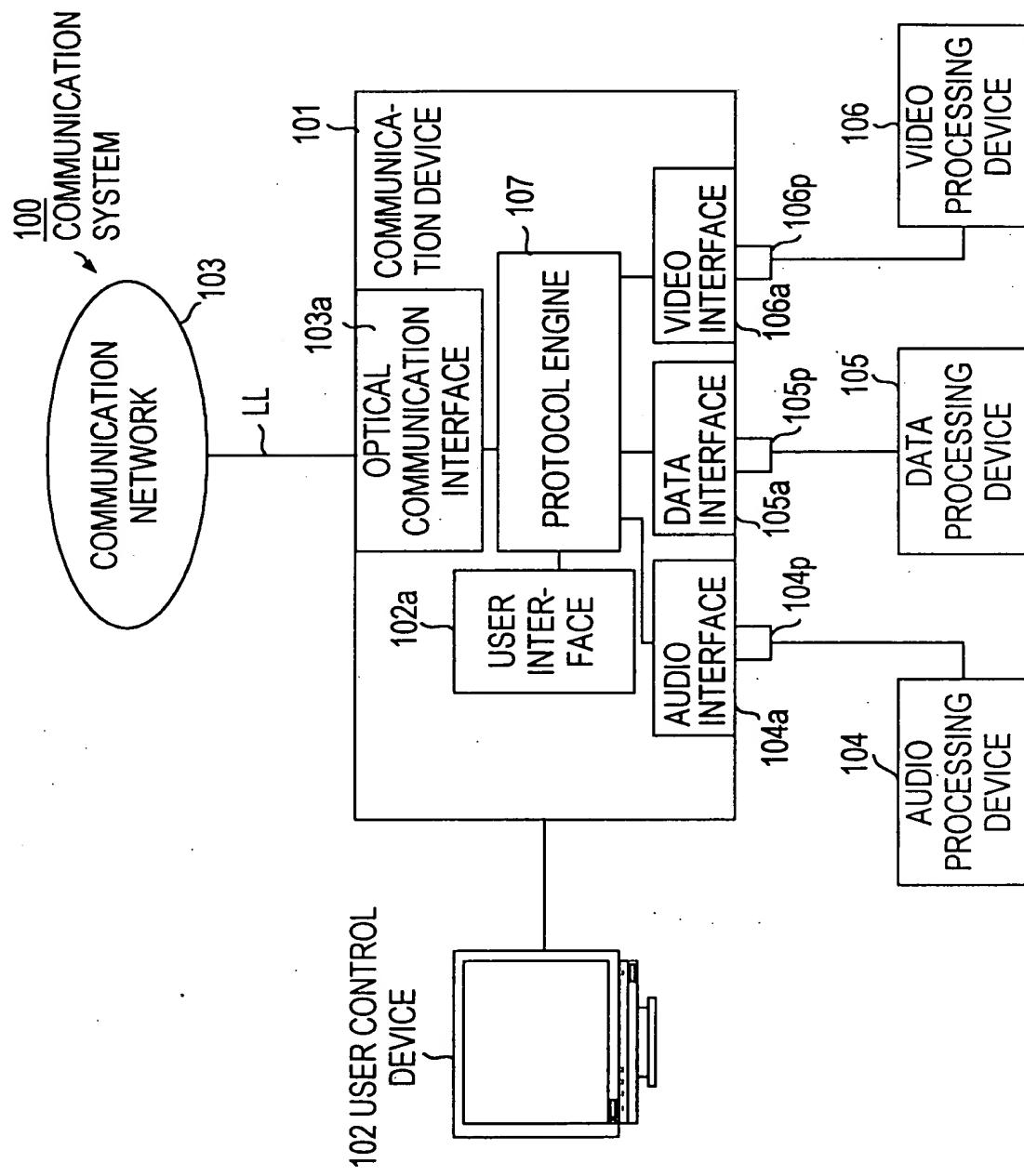


FIG. 9 (PRIOR ART)

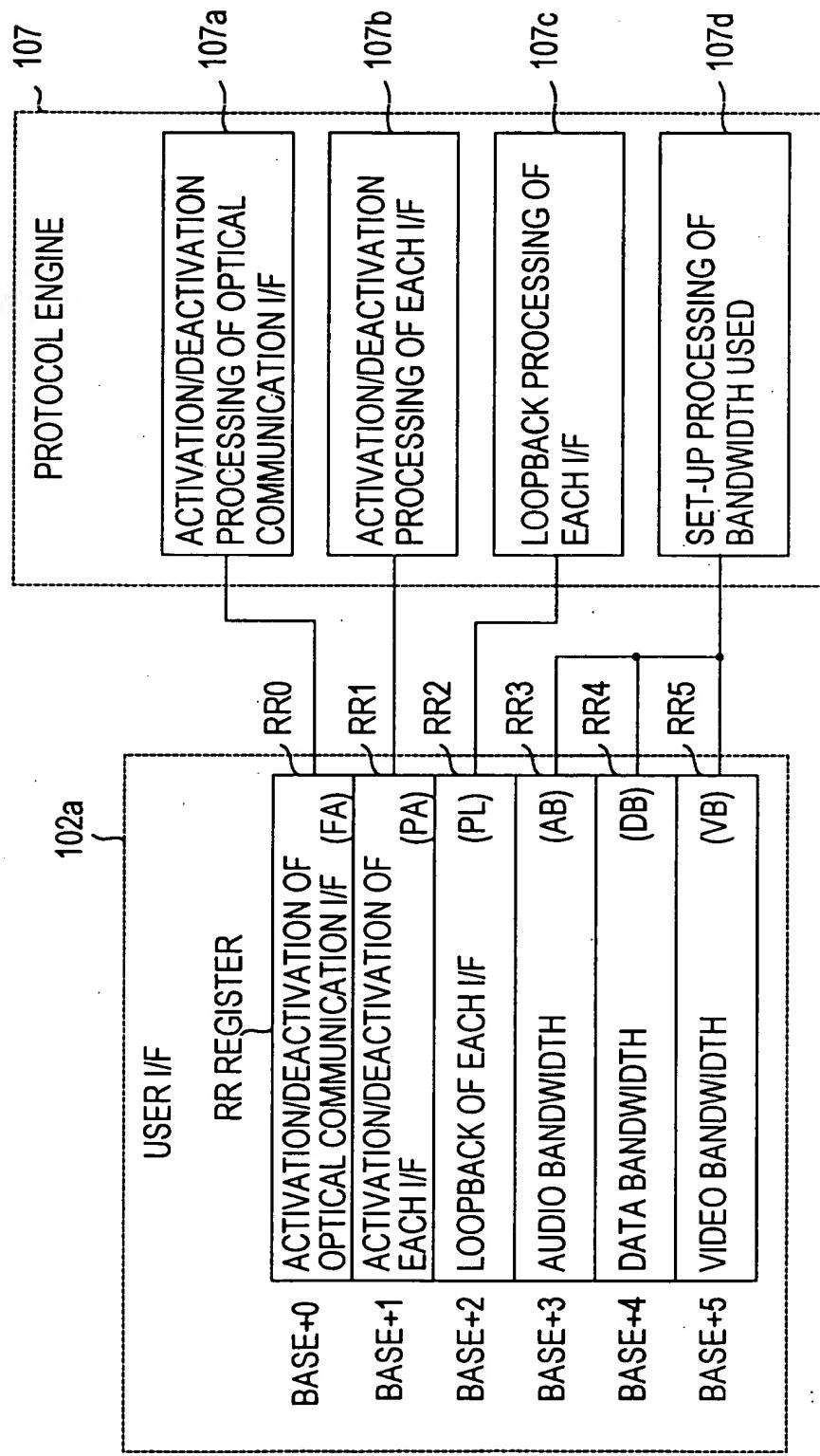


FIG. 10 (PRIOR ART)

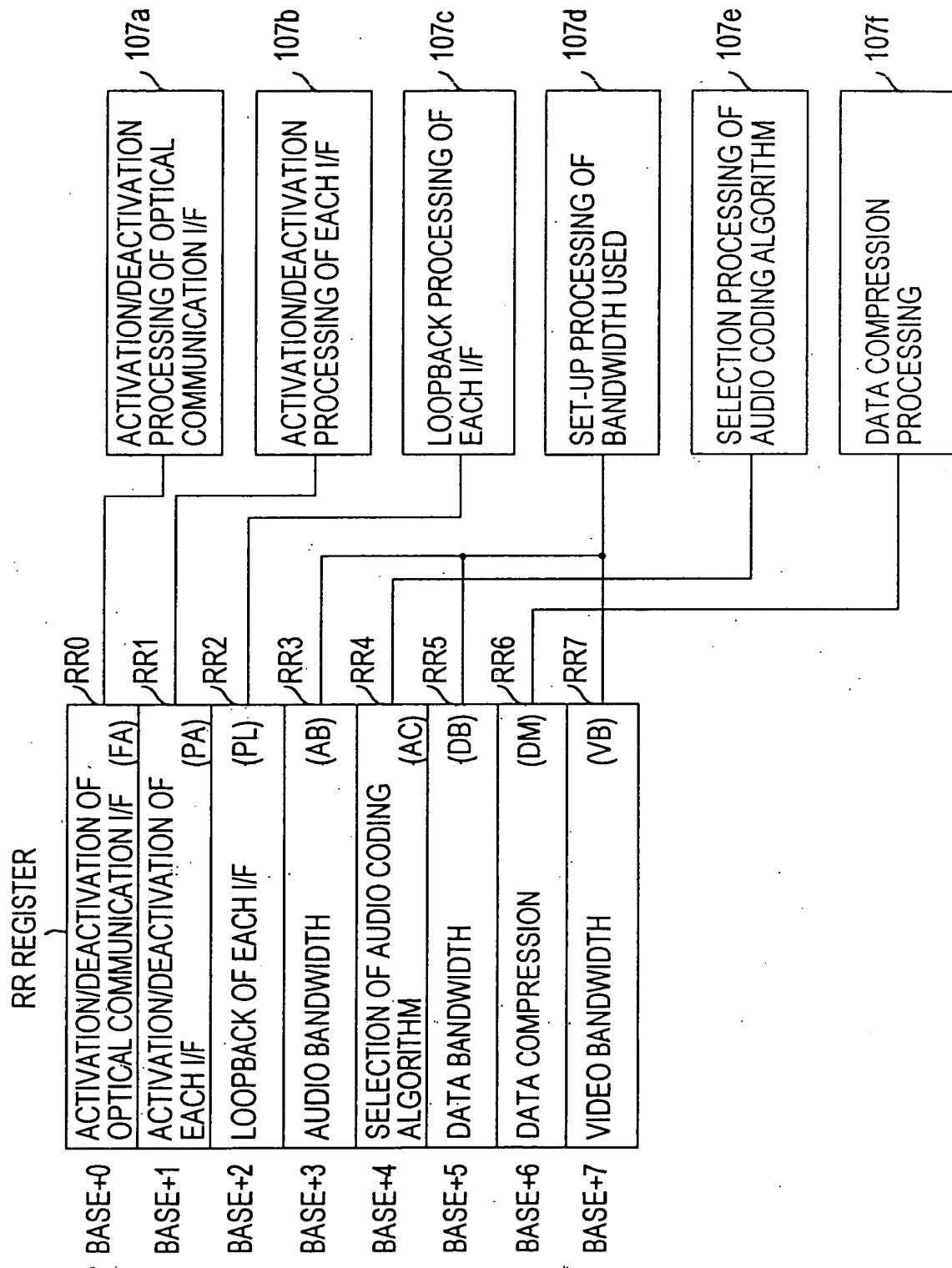


FIG. 11 (PRIOR ART)